

## Electronic Patent Application Fee Transmittal

Application Number:	09943512			
Filing Date:	31-Aug-2001			
Title of Invention:	Multilayer wiring board and semiconductor device			
First Named Inventor/Applicant Name:	Yasuo Osone			
Filer:	Gregory Edwards Montone/David Snyder			
Attorney Docket Number:	500.40530X00			
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>130</b>